

# **Dr. Shirshendu Roy**

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## **Academic Qualifications**

- Secondary – First Division, Fanindra Dev Institution, WBCHE, Jalpaiguri, West Bengal, India.
- Higher Secondary – First Division, Fanindra Dev Institution, WBHE, Jalpaiguri, West Bengal, India.
- B.E. (Electronics and Tele-Communication)- First Division, Bengal Engineering and Science University (BESU), Shibpur, Howrah, India.
- M.E. (Digital System and Instrumentation)-First Division, Indian Institute of Engineering Science and Technology (IEST), Shibpur, Howrah, India
- Ph. D. (VLSI Signal Processing) in Electronics and Communication Department, National Institute of Technology (NIT), Rourkela, Odisha, India.

## **Industrial Experience**

- Worked for Hindalco Industries Ltd. at MAHAN 6\*150 MW CPP, Singrauli as Assistant Manager in Control and Instrumentation department for 4 years.

## **Current Affiliation**

- Currently working as Assistant Professor in Gandhi Institute of Engineering and Technology University (GIETU), Gunupur, Odisha, India.

## **Journals Published**

1. S. Roy, D. P. Acharya and A. K. Sahoo, "Low-Complexity Architecture of Orthogonal Matching Pursuit Based on QR Decomposition," *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol. 27, no. 7, pp. 1623-1632, July 2019, doi: 10.1109/TVLSI.2019.2909754.
2. S. Roy, D. P. Acharya and A. K. Sahoo, "Incremental Gaussian Elimination Approach to Implement OMP for Sparse Signal Measurement," *IEEE Transactions on Instrumentation and Measurement*, vol. 69, no. 7, pp. 4067-4075, July 2020, doi: 10.1109/TIM.2019.2947118.
3. S. Roy, D. P. Acharya, and A. K. Sahoo, "Fast OMP Algorithm and its FPGA implementation for Compressed Sensing Based Sparse Signal Acquisition Systems," *IET Circuits, Devices and Systems*, March, 2021, <https://doi.org/10.1049/cds2.12047>.